

IN THE DRAWINGS

Please approve the changes to the drawings as outlined in the attached Letter to the Draftsperson, and as shown in the accompanying revised replacement drawings. Specifically, in Figure 2, the label "PRIOR ART" is being added.

REMARKS

The above amendments to the above-captioned application along with the following remarks are being submitted as a full and complete response to the Office Action dated March 1, 2005 (U.S. Patent Office Paper No. 5). In view of the above amendments and the following remarks, the Examiner is respectfully requested to give due reconsideration to this application, to indicate the allowability of the claims, and to pass this case to issue.

Status of the Claims

As outlined above, claim 2 is currently pending in this application, wherein claim 1 is being canceled without prejudice or disclaimer, while claims 3-17 stand withdrawn from further consideration. Claim 2 is being amended into independent form, to correct formal errors and to more particularly point out and distinctly claim the subject invention.

Additional Amendments

The drawings are being amended to correct formal errors and to better disclose and describe the features of the present invention as claimed. Specifically, Figure 2 is being labeled as "PRIOR ART." Applicant hereby submits that no new matter is being introduced into the application through the submission of this response.

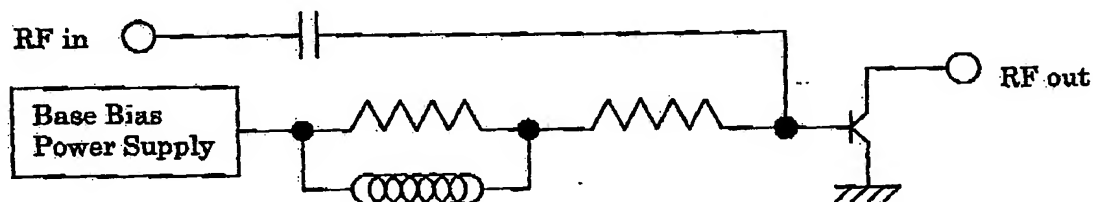
Prior Art Rejections

Claims 1 and 2 were rejected under 35 USC §102(e) as being anticipated by US Patent No. 6,727,761 to Apel. Applicants have reviewed this reference and hereby respectfully traverse.

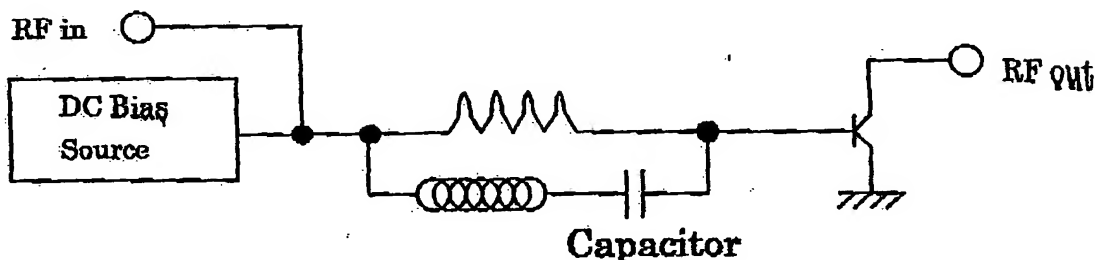
The present invention as now claimed is directed to a radio frequency power amplifier, comprising: a bipolar transistor for emitter ground amplification having a base coupled to an input terminal and a base bias power supply and having a collector coupled to an output terminal, wherein a resistor and a coil are inserted parallel between the base and the base bias power supply, and the coil is connected to the base so that a direct current can be conducted through a section between the coil and the base.

According to the present invention, a direct current (DC) component out of the current components that are sent from a bias circuit through a base of a transistor passes through a bypassed coil without passing through a bias resistor. Further, the coil is connected to the base so that the DC can be conducted through a section between the coil and the base, and

only low-frequency components is allowed to pass through the bias resistor. Accordingly, the present invention prevents distortion from increasing due to a base voltage drop caused by the DC component passing through the bias resistor. See page 8, lines 1-17 (especially, lines 7-13) and Figure 4 of the present application as illustrated below:



In contrast, Apel '761 discloses in its Figure 3 an electrical circuit incorporating resonant bypassed base ballast resistors, the circuit having capacitors 116 and 120 connected in series to inductors 114 and 118, respectively. Each ballast resistor is bypassed with a series-coupled inductor and capacitor that provide a resonant, low-loss RF signal current path between the input node and the base terminals of the transistor, as illustrated below:



Apel '761 does not disclose or teach an electrical circuit in which the inductor is connected to the base so that DC can be conducted through the section between the inductor and the base, and consequently, cannot and does not disclose or teach anything about the distortion increase due to a gain decrease.

The circuit by Apel cannot achieve the effect of suppressing the distortion increase due to the gain decrease even if it can achieve the effect of low-loss RF signal current path, because DC is cut off at the capacitor connected in series to the inductor and then the cut-off DC passes through the bias resistor. See Apel '761 at column 2, lines 28-43 (especially, lines 31-34) and Figures 3-4.

As a result, the present invention cannot be anticipated or rendered obvious in view of Apel '761, since the present invention as claimed is directed to a circuit configuration very different in structure and operation from those of the circuits shown by Apel '761. The present invention as claimed is thus distinguishable and thereby allowable over the prior art of Apel '761.

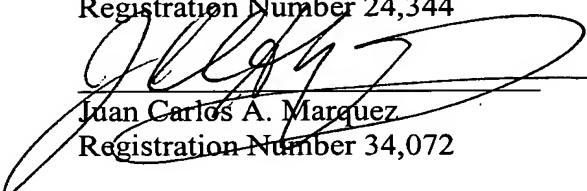
Conclusions

In view of all the above, Applicant respectfully submits that certain clear and distinct differences as discussed exist between the present invention as now claimed and the prior art references upon which the rejections in the Office Action rely. These differences are more than sufficient that the present invention as now claimed would not have been anticipated nor rendered obvious given the prior art. Rather, the present invention as a whole is distinguishable, and thereby allowable over the prior art.

Favorable reconsideration of this application as amended is respectfully solicited. Should there be any outstanding issues requiring discussion that would further the prosecution and allowance of the above-captioned application, the Examiner is invited to contact the Applicant's undersigned representative at the address and phone number indicated below.

Respectfully submitted,

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